

R09

Code No: C7009

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.TECH I SEMESTER EXAMINATIONS APRIL/MAY-2012**

**ADVANCED COMPUTER ARCHITECTURE
(ELECTRONICS AND COMMUNICATION ENGINEERING)**

Time: 3hours

Max.Marks:60

**Answer any five questions
All questions carry equal marks**

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- 1.a) What is meant by addressing mode? Discuss various addressing modes.
- b) List the types of operands.
- 2.a) What is an instruction cycle? Explain.
- b) Give a note on RISC and CISC controversy.
- 3.a) Discuss associate memory organization.
- b) Compare and contrast paging with segmentation.
- 4.a) Explain interrupt driven I/O.
- b) Describe the functionality of IO processor.
- 5.a) Discuss the problems in parallel processing.
- b) Differentiate between horizontal and vertical instruction formats.
- 6.a) Describe distributed shared memory.
- b) Explain the significance of RAID. Briefly discuss its levels.
7. Discuss the practical issues in interconnecting networks with suitable illustrations.
- 8.a) Explain the necessary hardware support for more ILP at compile time.
- b) Describe thread level parallelism and synchronization.

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